

Implémentation Des Algorithmes de Traitement du Signal sur carte FPGA

S.AISSAOUI, S. MOKHTARI

Soutenue en: 2021

Abstract: The aim of this work is to realize an on-board system allowing to automate the control in NDT by ultrasonic. For this, we performed the transmission and reception of signals using an FPGA card that acquisition and signal processing are implemented on the same FPGA circuit.

Keywords : Non-destructive testing (CND), ultrasonic signals, FPGA, Signal processing